

SESSION THREE



Multi-Core and Parallelism II

CHAIR ALAN JAY SMITH, UC BERKELEY

3:10 - 3:20 PM

AMD's Radeon™ HD 2900 - 2nd Generation Unified Shader Architecture

Mike Mantor
AMD

Teraflops Prototype Processor with 80 Cores

**Yatin Hoskote,
Sriram Vangal,
Saurabh Dighe,
Nitin Borkar,
Shekhar Borkar**
Intel

TRIPS: A Distributed Explicit Data Graph Execution (EDGE) Microprocessor

**Madhu Saravana,
Sibi Govindan,
Doug Burger, Steve
Keckler, and the
TRIPS team.**
UT - Austin

The Tile Processor™ Architecture: Embedded Multicore for Networking and Digital Multimedia

**Anant Agarwal,
et al**
Tilera Corporation

